

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	39	clock adj data same retim\$4 adj circuit	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/04/03 07:34
L2	1	clock adj data same retim\$4 adj circuit and loss adj4 signal	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/04/03 07:34
L3	12	clock adj data and retim\$4 adj circuit and loss adj4 signal	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/04/03 07:38
L4	1	("5563893").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/04/03 08:02
L5	0	(327/205).cds. and clock adj data and hysteresis with (bit adj error adj rate or BER)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/04/03 08:07
L6	1	(327/205,2,37,68,147,152,156-159,161,205).cds. and clock adj data and compar\$4 with (bit adj error adj rate or BER)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/04/03 08:08
L7	1	(327/205,2,37,68,147,152,156-159,161,205).cds. and (PLL or clock adj data) and compar\$4 with (bit adj error adj rate or BER)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/04/03 08:08
L8	10	(375/373-376,327).cds. and (PLL or clock adj data) and compar\$4 with (bit adj error adj rate or BER)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/04/03 08:09
L9	2	(370/516-519).cds. and (PLL or clock adj data) and compar\$4 with (bit adj error adj rate or BER)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/04/03 08:09
L10	8	(398/25,27,17,155,158).cds. and (PLL or clock adj data) and compar\$4 with (bit adj error adj rate or BER)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/04/03 08:09
S1	555	phase adj (compar\$4 or detect\$4) with delay with clock with data	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/03/23 10:29

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S2	21	phase adj (compar\$4 or detect\$4) with delay with clock with data with (outside or before)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/03/21 16:47
S3	10	phase adj (compar\$4 or detect\$4) with delay with clock with data with fall\$4 adj4 edge	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/03/21 16:47
S4	5	phase adj (compar\$4 or detect\$4) with delay with fall\$4 adj4 edge near4 clock with data	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/03/21 16:52
S5	256	clock same delay\$4 with data same flip adj4 flop same integrat\$4	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/03/21 16:53
S6	125	clock same delay\$4 with data same flip adj4 flop with integrat\$4	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/03/21 16:53
S7	125	clock same delay\$4 with data same flip adj flop with integrat\$4	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/03/21 16:53
S8	64	clock same delay\$4 with data with flip adj flop with integrat\$4	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/03/21 16:53
S9	50	clock with delay\$4 with data with flip adj flop with integrat\$4	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/03/21 16:55
S10	6	clock with delay\$4 with data with flip adj flop with integrator	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/03/21 16:56
S11	8	clock with delay\$4 with data with flip adj flop with (integrator or integrater or limiter)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/03/21 17:00
S12	0	delay near4 data same (flip adj flop or phase adj detect\$4) with clock same integrater	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/03/21 17:01
S13	19	delay near4 data same (flip adj flop or phase adj detect\$4) with clock same integrator	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/03/21 18:03

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S14	2	delay near4 data same (flip adj flop or phase adj detect\$4) with clock same integrator and compar\$4 with threshold	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/03/21 18:04
S15	19	delay near4 data same (flip adj flop or phase adj detect\$4) with clock and integrator and compar\$4 with threshold	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/03/22 09:42
S16	478	phase adj (compar\$4 or detect\$4) with fall\$4 adj4 edge	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/03/22 09:43
S17	33	phase adj (compar\$4 or detect\$4) with fall\$4 adj4 edge with period	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/03/22 09:44
S18	281	phase adj (compar\$4 or detect\$4) with fall\$4 adj4 edge with clock	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/03/22 09:45
S19	29	phase adj (compar\$4 or detect\$4) with fall\$4 adj4 edge with clock with flip adj flop	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/03/22 09:47
S20	0	phase adj (compar\$4 or detect\$4) with fall\$4 adj4 edge with clock with flip adj flop with error	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/03/22 09:47
S21	0	phase adj (compar\$4 or detect\$4) with fall\$4 adj4 edge with clock with error same flip adj flop	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/03/22 09:48
S22	5	phase adj (compar\$4 or detect\$4) with edge with clock with error same flip adj1 flop	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/03/22 12:41
S23	212	phase adj (compar\$4 or detect\$4) with edge with clock same flip adj1 flop	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/03/22 12:42
S24	141	phase adj (compar\$4 or detect\$4) with edge with clock with flip adj1 flop	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/03/22 12:43
S25	62	phase adj (compar\$4 or detect\$4) with edge with data with clock with flip adj1 flop	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/03/22 16:54

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S26	1	("4881243").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/03/22 16:55
S27	13	("4881243").URPH.	USPAT; USOCR; EPO; JPO	OR	OFF	2006/03/22 16:55
S28	1	("20030164724").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/03/23 10:29
S29	0	S28 and symetric\$4	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/03/23 10:29
S30	1	S28 and symmetrical	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/03/23 11:07
S31	2808	integrat\$4 with phase adj detect\$4	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/03/23 11:07
S32	56	integrat\$4 with phase adj detect\$4 with reset\$4	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/03/23 11:10
S33	28	integrator with phase adj detect\$4 with reset\$4	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/03/23 11:10
S34	0	phase adj detect\$4 with flip adj flop with delay with period	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/03/24 10:30
S35	0	phase adj detect\$4 with flip adj flop with delay with period same capacitor	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/03/24 10:30
S36	0	phase adj detect\$4 with flip adj flop with delay same capacitor	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/03/24 10:30
S37	316	phase adj detect\$4 with delay same capacitor	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/03/24 10:31

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S38	2	phase adj detect\$4 with delay with reduc\$4 with capacitor	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/03/24 10:31
S39	2	phase adj detect\$4 with delay with (reduc\$4 or minimi\$4) with capacitor	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/03/24 11:07
S40	0	Integrat\$4 with reset\$4 with switch with bit adj4 error adj4 rate	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/03/24 11:08
S41	2	Integrat\$4 with reset\$4 with switch with (ber or bit adj4 error adj4 rate)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/03/24 11:09
S42	10	Integrat\$4 with reset\$4 with switch same (ber or bit adj4 error adj4 rate)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/03/24 11:09
S43	12	Integrat\$4 with reset\$4 with (ber or bit adj4 error adj4 rate)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/03/24 11:09
S44	93	Integrat\$4 with reset\$4 same (ber or bit adj4 error adj4 rate)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/03/24 11:12
S45	0	Integrat\$4 with reset\$4 same (ber or bit adj4 error adj4 rate) same phase adj detect	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/03/24 11:09
S46	2	Integrat\$4 with reset\$4 same (ber or bit adj4 error adj4 rate) same phase adj detect\$4	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/03/24 11:10
S47	2	Integrat\$4 with reset\$4 same (ber or bit adj4 error adj4 rate) same phase adj (compar\$4 or detect\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/03/24 11:10
S48	5	Integrat\$4 with reset\$4 same (ber or bit adj4 error adj4 rate) and phase adj (compar\$4 or detect\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/03/24 11:10
S49	12	Integrat\$4 with reset\$4 same (ber or bit adj4 error adj4 rate) same flip adj flop	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/03/24 11:18

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S50	337	Integrator with capacitor with current adj source	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/03/24 11:19
S51	79	Integrator with capacitor with current adj source with switch	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/03/24 11:19
S52	15	Integrator with capacitor with current adj source with (first with second) with switch	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/03/24 11:33
S53	0	Integrator with capacitor with current adj source with (first with second) with switch with phase adj detect\$4	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/03/24 11:34
S54	0	Integrator with capacitor with current adj source with (first with second) with switch with (phase with error or phase adj detect\$4 or phase adj compar\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/03/24 11:35
S55	0	Integrator with capacitor with current adj source with (first with second) with switch same (phase with error or phase adj detect\$4 or phase adj compar\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/03/24 13:12
S56	27	c3mos	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/03/24 23:53
S57	1098	polyphase adj filter\$4	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/03/24 23:54
S58	650	polyphase adj filter\$4 and receiver	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/03/24 23:56
S59	129	(plurality or multiple) with polyphase adj filter\$4 and receiver	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/03/24 23:56
S60	1210	Integrat\$4 same current adj source same capacitor same switch	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/04/02 19:39

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S61	17	Integrat\$4 same current adj source same capacitor same switch and clock adj data	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/04/02 19:43
S62	65	Integrat\$4 same current adj source same capacitor same first with second near4 switch	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/04/02 19:43
S63	4	Integrat\$4 same current adj source same capacitor same first with second near4 switch and pll	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/04/02 19:43
S64	0	Integrat\$4 same current adj source same capacitor same first with second near4 switch and loss adj4 signal	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/04/02 19:43